		~	~	_							
		Subject (e: BN	4ICA	.010)1				
		Roll. No:	 								
MOIDA	INSTITUTE OF ENGINEERING A	ND TEC	LINI		 GV	CD CD	E A 7	LED	N/O		
NOIDA	(An Autonomous Institute Affi							ICK	NO	IDA	
	MCA (Inte		7 111	10,	Luch		•)				
	SEM: I - THEORY EXAMI	_	N (20	024 -	202	5)					
	Subject: Digital Logic	c & Circu	ait E	esig)	n						
Time: 3 Hou								Max	k. M	[arks:	100
General Instru		mar with	tha	corr	act c	0111	ga a	odo	bra	nch a	nto.
	hat you have received the question pa ion paper comprises of three Sections	_									ic.
	(CQ's) & Subjective type questions.	, 11, D, Q	· •.	11 00	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	<i>3 0 j</i>	11100	ripic		orce	
	marks for each question are indicated	d on right	t -hc	ınd s	ide o	f ea	ch q	uest	ion.		
•	our answers with neat sketches where	ever nece	essai	ry.							
	table data if necessary.										
•	write the answers in sequential orde		a 1.1		.l4	:1	1	4 1			
o. No sneet sn evaluated/che	nould be left blank. Any written mater ocked	rai ajier	a vi	ank s	sneet	WII	i no	i be			
evaniarea, ene	encu.										
SECTION-A	<u>.</u>										20
1. Attempt all	parts:-										
_	B=B+A is called as (CO1,K	(1))					1
(a) A	Associative law										
(b)	Commutative law										
(c)]	Idempotent law	(),									
(d)]	None										
1-b. Gra	y code of binary number 0101 is: (C	O1,K1)									1
(a) (0010										
(b) (0001										
(c) (0111										
(d) (0110										
1-c. A co	ombinational circuit that takes only 1	input an	ıd gi	ves 1	nulti	ple	outr	out is			1
	D2,K1)	1	Ü			•	•				
(a) I	MUX										
(b)]	DEMUX										
(c) I	Decoder										
(d)]	Encoder										
1-d. A co	ode converter is a logic circuit that _	((CO	2,K1)						1
(a) l	Inverts the given input										
	Converts into decimal number										

	(c)	Converts data of one type into another type	
	(d)	Converts to octal	
1-e.	L	atch is the basic building block of: (CO3,K1)	1
	(a)	Flip flop	
	(b)	Buffer	
	(c)	Gate	
	(d)	None	
1-f.		he arithmetic operation that can be performed by doing a Left shift is: CO3,K1)	1
	(a)	Addition	
	(b)	Subtraction	
	(c)	Multiplication	
	(d)	Division	
1-g.	T	he diagram that shows the transition of states is known as: (CO4,K1)	1
	(a)	Transition diagram	
	(b)	State Table	
	(c)	Both of above	
	(d)	None	
1-h.	A	type of FSM where the outputs depend only on the current state. (CO4,K1)	1
	(a)	Deterministic FSM	
	(b)	Mealy machine	
	(c)	Moore machine	
	(d)	Non- Deterministic	
1-i.	T	he fundamental building blocks of IoT networks are: (CO5,K1)	1
	(a)	Sensors	
	(b)	Object	
	(c)	Physical thing	
	(d)	Sensor network	
1-j.	G	PS stands for: (CO5,K1)	1
	(a)	Global positioning system	
	(b)	Global parity system	
	(c)	Geo positioning system	
	(d)	Global position software	
2. Att	empt a	all parts:-	
2.a.	Fi	and the decimal equivalent of (101101.1)2 (CO1,K2)	2
2.b.	D	raw the circuit diagram of 1-bit magnitude comparator. (CO2,K1)	2
2.c.	Н	ow many types of shift registers are there and write their names also? (CO3,K1)	2
2 d	XX	Trite the significance of State Table (CO4 K2)	

2.e.	Write some advantages of IoT. (CO5,K2)	2			
SECTIO	<u>N-B</u>	30			
3. Answe	er any <u>five</u> of the following:-				
3-a.	Perform A-B using 1's and 2's complements for A= 1100 and B= 0111. (CO1,K2)				
3-b.	Discuss with truth table NAND, NOR &, EXOR gates. (CO1,K2)	6			
3-c.	Design a full Adder using two half adders. (CO2,K2)	6			
3-d.	Design a 3-bit Binary to Gray code convertor. (CO2,K3)	6			
3.e.	Discuss Ring counter and its working. (CO3,K2)				
3.f.	Compare Mealy and Moore Machines in detail. (CO4,K2)				
3.g.	Discuss the following: a) Temperature sensor b) Proximity sensor and c) IR sensor (CO5,K2)	6			
SECTIO	<u> </u>	50			
4. Answe	er any one of the following:-				
4-a.	Simplify the function $F(A,B,C,D) = \sum (0,,2,5,7,8,10,13,15) + d(6,14)$ in SOP form and implement using logic gates. (CO1,K3)				
4-b.	Explain the following terms with suitable example: a) Maxterm and b) Minterm (CO1,K3)	10			
5. Answe	er any <u>one</u> of the following:-				
5-a.	Implement F (A, B, C, D) = \sum (0,1,3,4,8,9,11,15) using 8x1 multiplexer: If MSB i.e. A is used as input variable and B, C, D as select lines. (CO2,K3)	10			
5-b.	Implement full subtractor using Decoder. (CO2,K3)	10			
6. Answe	er any <u>one</u> of the following:-				
6-a.	Realize T flip flop using D flip flop. (CO3,K3)	10			
6-b.	Design MOD-5 synchronous counter. (CO3,K3)	10			
7. Answe	er any one of the following:-				
7-a.	Explain static and dynamic hazard with proper examples. (CO4,K3)	10			
7-b.	Explain propagational delay and its role in hazard occurrence. (CO4,K3)	10			
8. Answe	er any <u>one</u> of the following:-				
8-a.	Discuss the core components of Machine-to- machine (M2M) architecture and explain their roles in enabling seamless communication between devices. (CO5,K3)				
8-b.	With circuit diagram, explain the working of Node MCU board. (CO5,K3)				